



सीएसआईआर-केन्द्रीय इलेक्ट्रॉनिकी अभियांत्रिकी अनुसंधान संस्थान  
CSIR - CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE  
(विज्ञान तथा प्रौद्योगिकी मंत्रालय, भारत सरकार/MINISTRY OF SCIENCE & TECHNOLOGY, GOVT. OF INDIA)  
पिलानी, राजस्थान (भारत)/Pilani, Rajasthan - 333031 (INDIA)

Advertisement No. 02/2015 dated 27.03.2015

**Last Date for Receipt of Application: 28.04.2015**

## A unique opportunity for research career in Science & Technology

CSIR-Central Electronics Engineering Research Institute (CSIR-CEERI), Pilani is a premier research Institute in the field of Electronics, established in 1953 under the aegis of Council of Scientific & Industrial Research (CSIR). It is devoted to R&D activities in three areas, namely: (1) **Advanced Electronic Systems**: [Sub-areas: Electronic Instrumentation, Industrial Control & Automation, Power Electronics, Robotics, Image Processing & DSP, Embedded System Design etc.]; (2) **Advanced Semiconductor Electronics** [Sub-areas: MEMS, Microsensors, VLSI Design (Digital, Analog, Mixed Signal), Opto-electronic Technologies, Photonic Devices and Sub-systems, Nano-electronics etc.]; (3) **Microwave Tubes**: [Sub-areas: Klystron, Magnetron, Travelling Wave Tubes, Gyrotron, Plasma Tubes, Tera Hertz devices etc.]. CSIR-CEERI also has a Centre at Chennai.

Applications are invited from enthusiastic, young Indian researchers having excellent academic record and proven scientific achievements along with requisite experience and a high degree of motivation and desire to take up research as a career in the field of *Electronics*, to fill up vacancies for the following posts of Scientists at Pilani as well as Chennai:

Designation	No. of Posts	Pay Band	Grade Pay	*Total Emoluments	**Upper Age Limit not exceeding (as on 28.04.2015)
Scientist	UR 17 (including 1 PH-HH/OH)	₹15600-39100 (PB-3)	₹6600/-	₹61,035/-	32 years
Senior Scientist	OBC 9 (including 1 PH-HH/OH backlog) SC 5 ST 2 Total 33		₹7600/-	₹70,484/-	37 years
Principal Scientist	UR 3 Total 3	₹37400-67000 (PB-4)	₹8700/-	₹1,08,282/-	45 years
Senior Principal Scientist			₹8900/-	₹1,15,113/-	50 years

\* Total Emoluments mean approximate total emoluments on minimum of scale as on 01.01.2015 including House Rent Allowance (HRA) in Class 'Z' City. CSIR-CEERI may provide residential accommodation, if available, as per rules.

\*\* Please see age relaxation under Relaxation column.

The Selection Committee, depending upon the performance of the candidate, may choose to place the candidate in any of the Grade Pays within the same Pay Band subject to meeting the minimum eligibility criterion specified.

The details of posts are as given below:

Sr. No.	Post Code, No. of vacancies & Reservation status	Post Name	Essential Educational Qualifications and Experience	Relevant Field(s) of Experience
1.	AES1 02 posts (UR-01; SC-01)	Scientist  OR  Senior Scientist	M.E./M.Tech. (Power Electronics/Advanced Electronic Systems)  OR  Ph. D. (Engg.) submitted  M.E./M.Tech. (Power Electronics/Advanced Electronic Systems) with 03 years experience  OR  Ph. D. (Engg.)	Power electronic systems, renewable energy applications, motor drives and switch mode supplies.

2.	AES2 02 posts (UR-01;OBC-01)	Scientist  OR  Senior Scientist	M.E./M.Tech. (Electronics & Control/Instrumentation & Control/Electronics & Instrumentation/Mechanics/Advanced Electronic Systems) OR Ph. D. (Engg.) submitted  M.E./M.Tech. (Electronics & Control/Instrumentation & Control/Electronics & Instrumentation/Mechanics/Advanced Electronic Systems) with 03 years experience. OR Ph. D. (Engg.)	Advanced electronic control, robotic systems, process instrumentation and control, programmable logic controllers (PLCs), process modelling and automation, design and development of embedded electronic control systems, proficiency in MATLAB & Simulink, LAB view, PLC programming, C, C++, Linux and Windows.
3.	AES3 05 posts {UR-03;OBC-01; OBC-PH(HH/OH)-01}	Scientist  OR  Senior Scientist	M.E./M.Tech. (CS/CSE/Information & Communication Technology/ Information Technology /Advanced Information Technology/ECE/Electronics/ Instrumentation /I&C/Communication Systems/ Electronics & Instrumentation/Electronic Design Technology/VLSI System Design/ Embedded Systems/Advanced Electronic Systems) OR Ph. D. (Engg.) submitted  M.E./M.Tech. (CS/CSE/Information & Communication Technology/ Information Technology /Advanced Information Technology/ECE/Electronics/ Instrumentation /I&C/Communication Systems/ Electronics & Instrumentation/Electronic Design Technology/VLSI System Design/ Embedded Systems/Advanced Electronic Systems) with 03 years experience OR Ph. D. (Engg.)	<ul style="list-style-type: none"> <li>• Advanced 2D/3D image processing and computer vision algorithms and their implementation on FPGA/DSP boards; open CV, MatLab, C/C++/C#.</li> <li>• Electronic instrumentation/control system design using microcontroller/PC/ embedded platforms.</li> <li>• Wireless sensor networks, ZigBee, system verification, FPGAs, HDL, SDR, communication protocol stack, embedded systems.</li> <li>• Statistical/chemometric /spectroscopic data analysis, system identification/process modeling/optimization, classical/fuzzy/neural network modeling.</li> </ul>
4.	AES4- 04 posts (for Chennai Centre)  (UR-02; OBC-01; SC-01)	Scientist  OR  Senior Scientist	M.E./ M.Tech. (CS/CSE/Information & Communication Technology/ Information Technology /Advanced Information Technology/ECE/Electronics/ Instrumentation /I&C/Communication Systems/ Electronics & Instrumentation/Electronic Design Technology/ VLSI System Design/ Advanced Electronic Systems/Embedded Systems/ Renewable Energy) OR Ph. D. (Engg.) submitted  M.E./ M.Tech. (CS/CSE/Information & Communication Technology/ Information Technology /Advanced Information Technology/ECE/Electronics/ Instrumentation /I&C/Communication Systems/ Electronics & Instrumentation/Electronic Design Technology/ VLSI System Design/ Advanced Electronic Systems/Embedded Systems/ Renewable Energy) with 03 years experience. OR Ph. D. (Engg.)	<ul style="list-style-type: none"> <li>• Advanced 2D/3D image processing and computer vision algorithms and their implementation on FPGA/DSP boards; open CV, MatLab, C/C++/C#.</li> <li>• Electronic instrumentation/control system design using microcontroller/PC/ embedded platform.</li> <li>• Renewable Energy Sys.</li> <li>• Wireless sensor networks, ZigBee, system verification, FPGAs, HDL, Communication protocol stack, embedded systems.</li> <li>• Statistical/chemometric/ spectroscopic data analysis, NIR based instrumentation, system identification/process modeling/optimization, classical/fuzzy/neural network modeling.</li> </ul>

5.	ASE1(a) 01 post (UR)	Scientist  OR Senior Scientist	M.E./M.Tech. (VLSI/ IC Design/Microelectronics/Semiconductor Electronics/Advanced Semiconductor Electronics)  OR Ph. D. (Sc./Engg.) submitted  M.E./M.Tech. (VLSI/IC Design/Microelectronics/Semiconductor Electronics/Advanced Semiconductor Electronics) with 03 years experience  OR Ph. D. (Sc.) with 02 years experience  OR Ph. D. (Engg.)	EDA/CAD tools and/or FPGA tools, digital and/or mixed signal IC design.
6.	ASE1(b) 01 post (OBC)	Scientist  OR Senior Scientist	M.E./M.Tech. (CS/CSE/Software Engg./Information & Communication Technology/ Information Technology/ Advanced Information Technology/Advanced Electronic Systems/ Advanced Semiconductor Electronics)  OR Ph. D. (Engg.) submitted  M.E./M.Tech. (CS/CSE/Software Engg./Information & Communication Technology/ Information Technology/ Advanced Information Technology/Advanced Electronic Systems/ Advanced Semiconductor Electronics) with 03 years experience  OR Ph. D. (Engg.)	Computer networking, embedded software platforms, compilers and operating system platforms.
7.	ASE2 01 post(UR)	Scientist  OR Senior Scientist	M.E./M.Tech. (Electronics & Communication/Semiconductor Electronics/Advanced Semiconductor Electronics/VLSI design/Microelectronics/Solid State Technology)  OR Ph. D. (Sc./Engg.) submitted  M.E./M.Tech. (Electronics & Communication/Semiconductor Electronics/Advanced Semiconductor Electronics/VLSI design/Microelectronics/Solid State Technology) with 03 years experience  OR Ph. D. (Sc.) with 02 years experience  OR Ph. D. (Engg.)	Thick film & LTCC design tools and fabrication processes, Design and fabrication of special purpose packaging using LTCC/Hybrid Microcircuit technologies.
8.	ASE3 02 posts (UR-01; OBC-01)	Scientist  OR Senior Scientist	M.E./M.Tech. (Optoelectronics/Photonics/Materials Sc. & Technology/Laser Sc. and Technology/Optical Communication/Applied Physics/Solid State Technology/ Advanced Semiconductor Electronics/Semiconductor Electronics)  OR Ph. D. (Sc./Engg.) submitted  M.E./M.Tech. (Optoelectronics/Photonics/Materials Sc. & Technology/Laser Sc. and Technology/Optical Communication/Applied Physics/Solid State Technology/ Advanced Semiconductor Electronics/Semiconductor Electronics) with 03 years experience  OR Ph. D. (Sc.) with 02 years experience  OR Ph. D. (Engg.)	MOCVD based GaN material growth/material characterization/Optoelectronic/semiconductor device fabrication including devices like semiconductor LASERS, LEDs, Solar Cells, HEMTs etc.
9.	ASE4(a) 03 posts (UR-01; OBC-01; ST-01)	Scientist	M.E./M.Tech. (Nano-electronics/Nano-science and Technology/Micro-electronics/ Electronics/Semiconductor Technologies/Solid State Devices and Circuits/Advanced Semiconductor Electronics/Material Science & Technology/ Semiconductor Electronics)  OR Ph. D. (Sc./Engg.) submitted	Semiconductor device modeling/Simulation and technology, design, technology and characterization of MEMS/micro-sensors/micro/nano

		OR Senior Scientist	M.E./M.Tech. (Nano-electronics/Nano-science and Technology/Micro-electronics/ Electronics/Semiconductor Technologies/Solid State Devices and Circuits/Advanced Semiconductor Electronics/Material Science & Technology/ Semiconductor Electronics) with 03 years experience OR Ph. D. (Sc.) with 02 years experience OR Ph. D. (Engg.)	fluidic devices.
10.	ASE5 03 posts (UR-01; SC-01; UR-PH(HH/OH)- 01)	Scientist  OR Senior Scientist	M.E./M.Tech. (Nano-electronics/Micro-electronics/ Electronics/Semiconductor Technologies/Solid State Devices and Circuits/Nano Science & Technology/ Advanced Semiconductor Electronics/Material Science & Technology/Semiconductor Electronics) OR Ph. D. (Sc./Engg.) submitted  M.E./M.Tech. (Nano-electronics/Micro-electronics/ Electronics/Semiconductor Technologies/Solid State Devices and Circuits/Nano Science & Technology/ Advanced Semiconductor Electronics/Material Science & Technology/Semiconductor Electronics) with 03 years experience OR Ph. D. (Sc.) with 02 years experience OR Ph. D. (Engg.)	Nanoscience, nanostructures, CNT based devices, Bio-nano-sensors, SPR based sensors, polymer sensors, organic semiconductors, quodons, orbitons, design and technology of MEMS based sensors, material science, SiC detectors.
11.	MWT1(a) 01 post (OBC)	Scientist  OR Senior Scientist	M.E./M.Tech. (Microwave/High Power Microwave Devices & System Engineering) OR Ph. D. (Sc./Engg.) submitted  M.E./M.Tech. (Microwave/High Power Microwave Devices & System Engineering) with 03 years experience OR Ph. D. (Sc.) with 02 years experience OR Ph. D. (Engg.)	Plasma Physics and technologies relevant for high power plasma tubes, plasma devices and other plasma based electronic devices.
12.	MWT2(a) 07 posts (UR-03; OBC-01; SC-02; ST-01)	Scientist  OR Senior Scientist	M.E./M.Tech. (Microwave/High Power Microwave Devices & System Engineering) OR Ph. D. (Sc./Engg.) submitted  M.E./M.Tech. (Microwave/High Power Microwave Devices & System Engineering) with 03 years experience OR Ph. D. (Sc.) with 02 years experience OR Ph. D. (Engg.)	Design and technology of microwave tubes including cathodes, Klystron, Gyrotron , Travelling Wave Tubes, Magnetrons and vacuum microelectronic devices.
13.	MWT2(b) 01 post (UR)	Scientist  OR Senior Scientist	M.E./M.Tech. (Mechanical Engg./Fluids & Thermal Engineering/Design of Mechanical Equipment/ Computational Mechanics/Tools & Product Design) OR Ph. D. (Engg.) submitted  M.E./M.Tech. (Mechanical Engg./Fluids & Thermal Engineering/Design of Mechanical Equipment/ Computational Mechanics/Tools & Product Design) with 03 years experience OR Ph. D. (Engg.)	Solid mechanics, structural mechanics, heat transfer and thermal systems, machining and manufacturing processes, micro-machining techniques

14.	ASE4(b) 01 post (UR)	Principal Scientist  OR Senior Principal Scientist	Ph. D. (Sc./Engg.) with 03 years experience  Ph. D. (Sc./Engg.) with 06 years experience	Laboratory experience in Unit Processes and Technology of Semiconductor devices/microelectronic devices. MEMS, micro/nano sensors/Micro-fluidics: Design/technology/characterization.
15.	MWT1(b) 01 post (UR)	Principal Scientist  OR Senior Principal Scientist	Ph. D. (Sc./Engg.) with 03 years experience  Ph. D. (Sc./Engg.) with 06 years experience	Plasma Physics and technologies relevant for high power plasma tubes, plasma devices and other plasma based electronic devices.
16.	MWT2(c) 01 post (UR)	Principal Scientist  OR Senior Principal Scientist	Ph. D. (Sc./Engg.) with 03 years experience  Ph. D. (Sc./Engg.) with 06 years experience	Design and Technology development of Microwave tubes in general and Klystron in particular.

**UR: Unreserved; SC: Scheduled Caste; ST: Scheduled Tribe; OBC: Other Backward Class; PH: Physically Handicapped; HH: Hearing Handicapped; OH: Orthopedically Handicapped.**

**Apart from qualifications indicated above, any other recognized qualification, which is equivalent to the prescribed qualification, shall be treated at par with that qualification.**

**Job description for the above posts:** Selected candidates will have to undertake research, development and innovation activities in the areas of works of the Institute. Placement of candidates in different functional groups may also be done as per the requirement of the Institute keeping in mind the knowledge and experience of the selected candidates. In addition to the R&D activities, consultancy/sponsored/grant-in-aid projects are also to be handled either independently or in a team. Selected candidates as per the requirements may be required to take classes in the Laboratory unit of the Academy of Scientific & Innovative Research (AcSIR) which is offering Masters/Ph.D Degrees. Selected candidates will also have the opportunity to supervise Research Scholars of the Academy (AcSIR) leading to their Ph.D. Degree as per the norms of the Academy.

**General information and conditions:**

**1. Benefits under Council service:**

- a. These posts carry usual allowances i.e. Dearness Allowance (DA), House Rent Allowance (HRA), Transport Allowance (TA) etc. as admissible to Central Government employees and as made applicable to CSIR. Council employees are also eligible for accommodation of their entitled type as per the CSIR Residence Allotment Rules depending on availability in which case HRA will not be admissible.
- b. Scientists in Pay Band-3 and Pay Band-4 are eligible for 2 additional increments (without DA) and Professional update allowance of ₹10000/- and ₹20,000/- per annum respectively.
- c. In addition to the emoluments indicated against each category of posts, benefits such as applicability of New Pension Scheme 2004, reimbursement of Medical Expenses, Leave Travel Concession, Conveyance Advance and House Building Advance are also available as per CSIR rules.
- d. Scientists in CSIR are also permitted to undertake consultancy and sponsored R&D project activity. These activities give them scope to earn consultancy fee and honorarium as per CSIR guidelines governing these activities. Opportunities also arise for foreign deputation for training/presentation of papers/specific assignments etc.
- e. CSIR provides excellent opportunities to deserving candidates for career advancement under the Assessment Promotion scheme for Scientists.
- f. Deserving candidates may be considered for advance increment(s) as per CSIR Rules.

**2. Other conditions:**

- a. The applicant must be a citizen of India.
- b. All applicants must fulfill the essential requirements of the post and other conditions stipulated in the advertisement as on the last date of receipt of applications. They are advised to satisfy themselves, before applying, that they possess at least the essential qualifications laid down for various posts as on the last date of receipt of application. No enquiry asking for advice as to eligibility, will be entertained.
- c. The prescribed essential qualifications are the minimum and the mere possession of the same does not entitle candidates to be called for interview. The duly constituted Screening Committee will adopt its own criteria for short-listing candidates. The candidate should, therefore, mention in the application all his qualifications and experiences in the relevant area over and above the minimum prescribed qualification, supported with documents. Completion of Ph.D. degree will be reckoned from the date of issue of provisional certificate/notification.
- d. The application should be accompanied by self attested copies of the relevant educational qualification and experience if any. The prescribed qualification should have been obtained from recognized Universities/Institutions, etc. Incomplete applications/applications received but not accompanied with the required certificates/documents are liable to be rejected.
- e. In respect of equivalence clause in the Essential Qualifications, if a candidate is claiming a particular qualification as equivalent qualification as per the requirement of advertisement, then the candidate is required to produce order/letter in this regard, indicating the Authority (with number and date) under which it has been so treated otherwise the Application is liable to be rejected.
- f. The period of experience rendered by a candidate on part time basis, daily wages, visiting/guest faculty will not be counted while calculating the valid experience for short listing candidates for interview.
- g. If any document/certificate furnished is in a language other than Hindi or English, a transcript of the same duly attested by a Gazetted Officer or Notary must be submitted.
- h. The date for determining the upper age limit, qualifications and/or experience shall be the closing date prescribed for receipt of applications.
- i. The period of experience in a discipline/area of work, wherever prescribed, shall be counted after the date of acquiring the minimum prescribed educational qualifications for that Grade.
- j. Persons with disabilities (PWD) fulfilling the eligibility conditions prescribed under GOI instructions are encouraged to apply.
- k. In case a candidate is staying abroad, his/her candidature may be considered *in absentia* by the Selection Committee on his/her written request.
- l. The Selection Committee may choose to place candidate in any of the Grade Pay within the same Pay Band depending upon performance of the candidate and subject to meeting the minimum eligibility criterion specified.
- m. Only outstation candidates called and found eligible for interview will be paid to and fro single second class rail fare from the actual place of undertaking the journey or from the normal place of their residence whichever is nearer to the place of interview's Railway Station on production of Rail Tickets/Rail Ticket Numbers or any other proof of journey.
- n. Any discrepancy found between the information given in application and as evident in original documents will make the candidate ineligible for appearing in interview. Such candidate will also not be paid any fare.
- o. The decision of the CSIR-CEERI/CSIR in all matters relating to eligibility, acceptance or rejection of applications, mode of selection, conduct of examination/interview will be final and binding on candidates.
- p. Canvassing in any form and/or bringing any influence, political or otherwise, will be treated as a disqualification for the post.
- q. NO INTERIM ENQUIRY OR CORRESPONDANCE WILL BE ENTERTAINED.

### 3. Relaxations:

- a. The upper age limit is relaxable upto 05 years for SC/ST and 03 years for OBC as per Government of India orders in force only in those cases where the posts are reserved for respective categories, on production of relevant certificate in the prescribed format signed by the specified authority.
- b. Upper age limit is also relaxable upto five years for regular employees working in CSIR Laboratories/Institutes, Government Departments, Autonomous Bodies and Public Sector Undertakings.
- c. As per GOI provisions, age relaxation for Widows, Divorced Women and Women Judicially separated from Husbands who are not remarried, the upper age limit is relaxable up to the age of 35 years (upto 40 years for members of Scheduled Castes/Scheduled Tribes and upto 38 years for members belonging to the Other Backward Classes in respect of the posts reserved for them). Persons claiming age relaxation under this sub-para would be required to produce the following documentary evidence:
  - i) In case of Widow, Death Certificate of her husband together with the Affidavit that she has not remarried since.
  - ii) In case of divorced Woman and Woman judicially separated from her husband, a certified copy of the judgment/decreed of the appropriate Court to prove the fact of divorce or the judicial separation, as the case may be, with an Affidavit that she has not remarried since.
- d. Age relaxation to Physically Handicapped (PH) persons: Age relaxation of 5 years is allowed (total 10 years for SCs/STs and 8 years for OBCs in respect of the posts reserved for them) to blind, deaf-mute and orthopedically handicapped persons for appointment to Group 'A' posts/services. The persons claiming age relaxation under this sub-para would be required to produce a certificate issued by the competent authority in the prescribed proforma in support of their claims clearly indicating that the degree of physical disability is 40% or more. In any case, the appointment of these candidates will be subject to their being found medically fit in accordance with the standards of medical fitness as prescribed by the Government for each individual Group 'A' posts to be filled by Direct Recruitment by Selection.
- e. Relaxation in age, educational qualification and/or experience may be considered in case of exceptionally meritorious candidates or if sufficient number of candidates possessing the requisite qualification and/or experience are not available to fill up the posts.
- f. Relaxation of 5 years will also be permissible to those who had ordinarily been domiciled in the Kashmir Division of the State of Jammu and Kashmir during the period from 1-1-1980 to 31-12-1989 subject to production of relevant certificate from concerned authority.

### 4. How to apply:

- a. Candidates are required to apply in the prescribed format (application and synopsis) downloaded from CSIR-CEERI website or neatly typed on A4 size paper on one side only as per the format available on the website [www.ceeri.res.in](http://www.ceeri.res.in).
- b. Applications complete in all respects enclosing attested copies of all certificates, marks sheet of all semesters/years/aggregate, testimonials in support of age, educational qualifications, experience, caste/community/PWD, recent passport size photograph signed along with non-refundable application fee of ₹100/- (SC/ST/PH/Women/CSIR employees/Abroad Candidates are exempt from payment of application fee) in the form of crossed Demand Draft drawn on any nationalized bank valid for a period of three months in favour of "Director, CSIR-Central Electronics Engineering Research Institute" payable at the State Bank of Bikaner & Jaipur, Pilani Branch, Plani, should be sent in an envelope superscribed "APPLICATION FOR THE POST OF \_\_\_\_\_(Post Code\_\_\_\_\_)" by post to **The Administrative Officer, CSIR-Central Electronics Engineering Research Institute, Pilani-333031 (Rajasthan)** so as to reach on or before the last date. **The last date given above will be the same for the candidates belonging to far-flung areas as well.**
- c. The following details must be filled up on back side of Demand Draft (i) **Candidate's Name**, (ii) **Candidate's Category**, (iii) **Post Code Applied for**.
- d. In case of Universities/Institutes awarding CGPA/SGPA/OGPA grades etc., candidates are requested to convert the same into percentage based on the formula as per their University/Institute.
- e. Candidates applying for more than one post must submit **separate application form for each post indicating the Code Number of the post with separate Demand Draft(s) for each application.**

- f. Application once made will not be allowed to be withdrawn and fee once paid will not be refunded on any count nor shall it be held in reserve for any other recruitment or selection process.
- g. Applications from employees of Government Departments will be considered only if forwarded through proper channel, certified by the employer that the applicant, if selected will be relieved within one month of the receipt of the appointment offer. Also, vigilance clearance should be recorded. However, advance copy of the application may be submitted before the closing date. Applications routed through proper channel should reach CSIR-CEERI at the earliest. 'No Objection Certificate' must be submitted at the time of interview.
- h. Candidates should specifically note that applications received after the closing date for any reason whatsoever (such as envelopes wrongly addressed, delivered elsewhere, postal delay, not supported by copies of certificates, without application fee, etc.) will be summarily rejected. Any enclosure received separately subsequent to the receipt of the application cannot be connected therewith. No representation in this regard will be entertained. Furnishing of false, incorrect and wrong information will be a disqualification.
- i. Incomplete applications (i.e. without photograph, applicable testimonials and application fee, unsigned etc.) will not be entertained and will be summarily rejected.
- j. The details of short-listed candidates to be called for interview, date of interview, change of date, selection result, cancellation of one or all posts, corrigendum/addendum to advertisement, extension of the last date for receipt of applications etc. will be notified on CSIR-CEERI website. Therefore, candidates are advised to visit the website [www.ceeri.res.in](http://www.ceeri.res.in) for latest updates.

**5. Following documents MUST be attached along with application form sent by post:**

- a. Demand Draft of ₹100/- as application fee, where applicable.
- b. Colour photograph pasted on the form and signed across in full.
- c. Self attested photocopy of certificates relating to Date of Birth, educational qualifications, experience and Caste/Community/PWD/Widow/Divorced Woman/Woman judicially separated from husband, if applicable.

**Note: In case of discrepancies between the English version of this Advertisement and its Hindi translation, the English version shall prevail.**

Administrative Office



